

## System-on-chip (SOC) design principle



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### **Reviews**

*These types of publication is the greatest publication available. It really is filled with knowledge and wisdom Once you begin to read the book, it is extremely difficult to leave it before concluding.*  
*(Prof. Lenna Beatty III)*

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paperback. Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Paperback Pages Number: 474 Language: Chinese. System-on-chip (SOC) design principle for the general higher education Eleventh Five-Year national planning materials. System-on-chip (System On a Chip. referred to as SOC) is a new milestone in the development of microelectronic technology. The book introduces the design method of system-on-chip system-level design as the core platform of EDA tools. Book starting from the basic unit of circuit design. VHDL language as the basic design tools to discuss a typical digital integrated systems design. and system-on-chip to achieve two basic ways: semi-custom high-density programmable logic devices (HDPLD to achieve) the implementation and full-custom application specific integrated circuit (ASIC). The main contents include the system-on-chip (SOC) design principle: the basis of the integrated circuit technology and layout. CMOS digital circuits. hardware description language VHDL and digital system design. system integration chip architecture. high-density programmable logic devices. programmable system-on-chip (SOPC). ASIC design and test structure design. The book language is smooth. step-by-step discussion of the various aspects of the system chip. Each chapter is accompanied by exercises for after-school practice. System-on-chip (SOC) design principle can be used as the institutions of higher learning electronic information for advanced undergraduates and graduate textbooks. but also can be used as the reference data of the related field of engineering and technical personnel. Four Satisfaction guaranteed, or money back.



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